

### Document information

Info	Content
<b>Keywords</b>	Current Sensing, PowerMOS, SenseFET, Sense Ratio, Sense Resistor, Virtual Earth
<b>Abstract</b>	Current Sensing power MOSFETs provide an effective means of protecting automotive electronic circuits from over current conditions. This application note shows the principle of operation using Virtual Earth current sensing and Sense Resistor current sensing techniques.

**Revision history**

Rev	Date	Description
01	20040909	Initial version

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## 1. Introduction

Current sensing power MOSFETs provide an effective means of protecting automotive electronic circuits from over current conditions. They offer an almost lossless method of measuring the load current that eliminates the need for a current shunt resistor.

Philips has developed a range of current senseFET products to address the automotive market as shown in Table 1. All are based on our low  $R_{dson}$  TrenchMOS™ PowerMOS technology.

**Table 1: Philips SenseFET range**

Device	$R_{dson}$ max (m $\Omega$ )	Sense Ratio
BUK7105-40AIE	5	500:1
BUK7905-40AI	5	500:1
BUK7C06-40AITE	6	560:1
BUK7108-40AIE	8	500:1
BUK7107-55AIE	7	500:1
BUK7C08-55AITE	8	500:1
BUK7109-75AIE	9	500:1
BUK7C10-75AITE	10	500:1

## 2. Principle of Operation

Current SenseFET technology is dependent on the close matching of transistor cells within the powerMOS. TrenchMOS™ are made up of many thousands of transistor cells in parallel. Elements within the device are identical, and the drain current is shared equally between them. The more cells that are in parallel for a given chip area the lower the on-state resistance of the MOSFET will be. This principle has been the key driving force for automotive powerMOS for many years is well understood by both suppliers and customers alike.

Furthermore, it is possible to isolate the source connections of several cells from those of the majority, and bring them out onto a separate sense pin. The powerMOS can now be thought of as two transistors in parallel with a common gate and drain but separate sources (Figure 1). When the devices are turned on the load current will be shared in ratio of their on-resistances.

The sense cells pass only a small fraction of the total load current in proportion to the ratio of their areas. This ratio is typically 500:1.

The ratio of the current through the MainFET to the SenseFET is known as the 'sense ratio',  $n$ . This ratio is defined for the condition where the source and sense terminals are held at the same potential. An additional Kelvin connection to the source metallisation enables accurate determination of the source potential.



Current feedback to the virtual earth of the op-amp ensures that the sense and Kelvin terminals are held at the same potential so that the geometric ratio is measured.

$$V_{sense} = -I_D R_{sense} / n$$

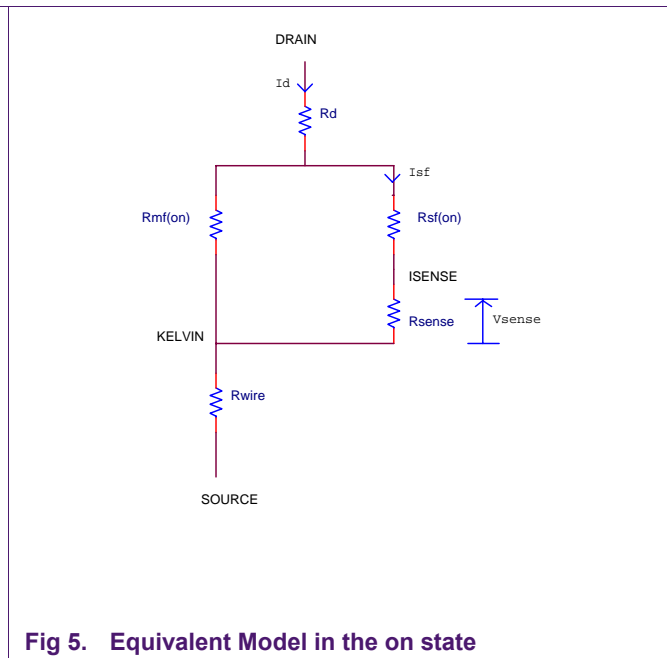
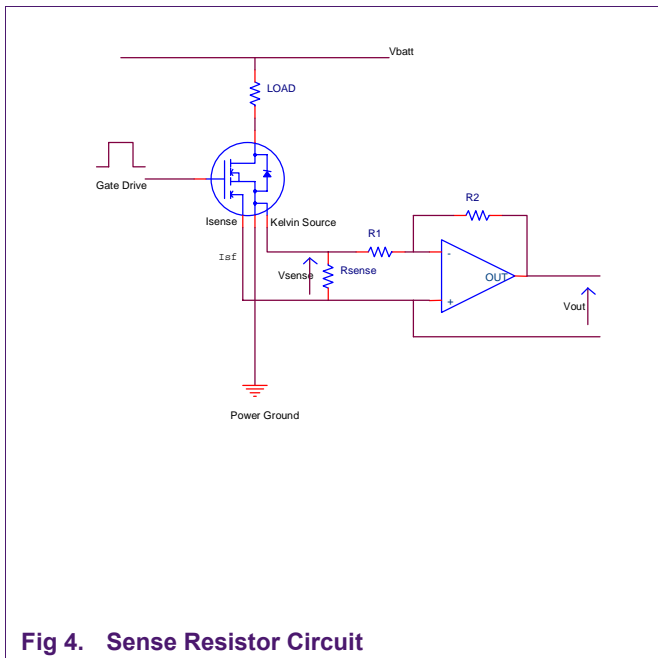
Equation 1

A typical response is shown in Figure 3. The sense signal closely mirrors the drain current throughout the whole pulse duration.

Tolerances of ±5% can be achieved with this circuit over temperature. The main advantage of this method is that the sense signal is independent of temperature and linearly proportional to load current. A negative supply is needed for the op-amp when the MOSFET is used in the low side.

#### 4. Sense Resistor current Sensing

The use of an external sense resistor in series with the sense pin offers a simpler technique for monitoring the current through the device (Figure 4 & Figure 5).



In figure 4 above, an external op-amp circuit is used to amplify the sense signal.

In Figure 5, the resistance of the FET is separated in to active and passive components, again with a common drain resistance. The active channels are modelled by  $R_{mf(on)}$  for the MainFET carrying the majority of the current and  $R_{sf(on)}$  for the senseFET. The passive contribution from the wire resistance is denoted  $R_{wire}$ .

The circuit operates as a potential divider with the following design equations.

$$V_{out} = \left(\frac{R_2}{R_1}\right) R_{sense} I_{sf}$$

**Equation 2**

$$V_{sense} = I_D R_{mf(on)} \frac{R_{sense}}{R_{sense} + R_{sf(on)}}$$

**Equation 3**

The inclusion of  $R_{sense}$  increases the resistance of the mirror leg and the sense ratio now becomes

$$n' = n \cdot \left(1 + \frac{R_{sense}}{R_{sf(on)}}\right)$$

**Equation 4**

The maximum voltage seen on the  $I_{sense}$  terminal occurs when the sense resistor is infinite i.e. open circuit condition. Known as the compliance voltage this equals

$$V_{DS(on)} \times R_{mf(on)} / (R_{mf(on)} + R_d)$$

Therefore the mirror terminal only samples a proportion of the full drain-source voltage. Fortunately for low voltage PowerMOS the contribution from the drain resistance is a small proportion of the total  $R_{ds(on)}$  and so the compliance ratio is high. This will deteriorate in higher voltage devices.

As an example the BUK7905-40AIE has  $R_{mf(on)} = 3\text{m}\Omega$ ,  $R_{sf(on)} = 1.1\text{m}\Omega$  and nominal sense ratio of 500:1. If  $R_{sense} = 1\Omega$  when 10A load current flows through the load this will generate  $V_{sense} \approx 14\text{mV}$ . In the low side a single rail amplifier can be used to amplify this signal up to a more useful level.

The Kelvin connection to source is essential for accurate current sensing. Otherwise voltage drops over the wire resistance that are caused by load current will add to the sense voltage and introduce a source of error. In the past this was less of an issue as a wire resistance of  $2\text{m}\Omega$  was only a small fraction of a  $200\text{m}\Omega$  mainFET. But modern PowerMOS products can have on-resistances as low as  $1\text{m}\Omega$ , comparable with the parasitic resistances. Referencing to the Kelvin pin eliminates the wire contribution.

The main disadvantage of the sense resistor technique is that the inclusion of  $R_{sense}$  introduces a temperature dependence.

Imagine the case where  $R_{sense} \rightarrow 0\Omega$ . The on-resistance of both the mainFET and senseFET track together over temperature and the ratio of the two remains constant. In this case the current sense ratio will also remain constant over temperature.

Conversely, if  $R_{sense}$  approaches infinity, the sense voltage now becomes  $V_{sense} = I_D R_{mf(on)}(T)$  (Equation 3), and will follow  $R_{mf(on)}$  over temperature. The mainFET on-resistance almost doubles between 25C and 175C thus eroding the tolerance of the measurement. For values between the two a balance must be struck between signal magnitude and accuracy. We normally recommend maintaining  $R_{sense} \ll R_{sf(on)}$  and amplifying the  $V_{sense}$  signal accordingly.

A typical response is shown in Figure 6.



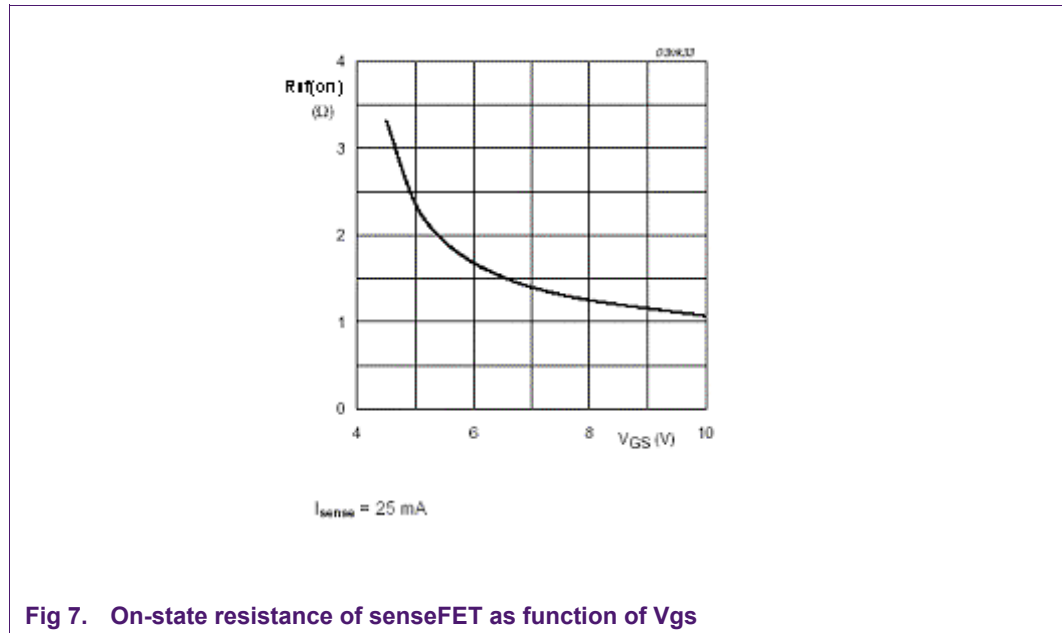


Fig 7. On-state resistance of senseFET as function of  $V_{GS}$

Normally designers choose to blank out the false peaks and sense the current once the device is fully enhanced.

## 5. Conclusion

SenseFET devices are an effective means of protecting automotive applications. They are a loss less and cost effective alternative to traditional current shunts whilst retaining realistic tolerances.

## 6. References

1. N.Zommer and J. Biran "Power current mirror devices and their applications." *Proc. Power convers. Int. Conf.* June., pp 275-283 (1986)





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Date of release: 09 September 2004

Published in The Netherlands